


<b>SUBMITTED BY</b>				Complete (if applicable)	
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Signature				Date	8/14/2006



IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Nicholas J. Kelsey, et al.  
SERIAL NO.: 09/748,098  
FILING DATE: December 21, 2000  
TITLE: System and Method for Instruction Level Multithreading in an Embedded Processor Using Zero-Time Context Switching  
EXAMINER: David J. Huisman  
GROUP ART UNIT: 2183  
ATTY. DKT. NO.: 20880-05093

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: 8/14/2006

By: Hector J. Ribera

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**AMENDMENT G AND REQUEST FOR CONTINUED EXAMINATION**  
**UNDER 37 C.F.R. § 1.114**

Sir:

This amendment for the patent application identified above is in response to the Office Action dated April 13, 2006, which set a shortened statutory period for response that expired on July 13, 2006, and to the Advisory Action mailed on June 29, 2006. A one-month extension of time until August 14, 2006 is hereby petitioned.

Kindly amend this application as indicated herein. A request for continued examination under 37 C.F.R. §1.114 is being filed concurrently herewith.